Iris II
(FR 362/606-10RCS)

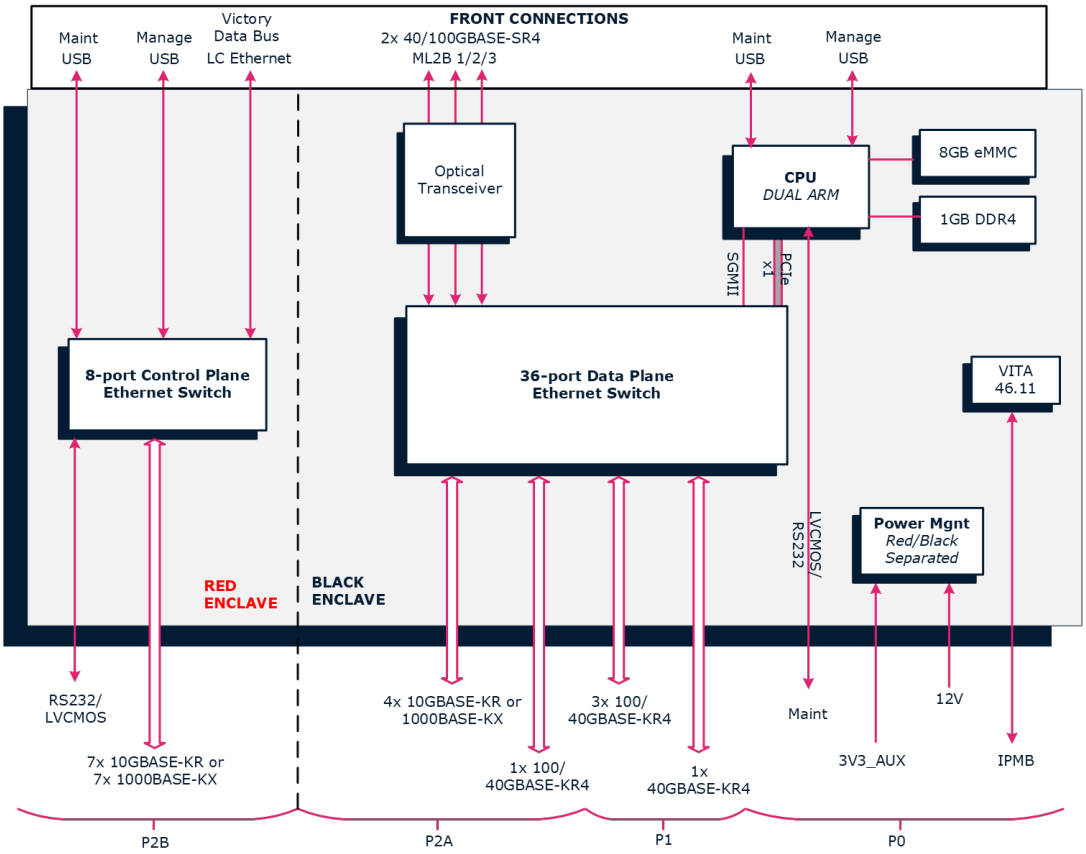
3U VPX™ Rugged 100/40/10 Gigabit Ethernet Dual Enclave Switch

Key Features

Iris II is the latest generation high speed, Layer 2/3+ switch aligned with the CMOSS, SOSA® and OpenVPX™ Technical Standards. Iris supports physically separate Data and Control Plane switching capabilities for high security applications.



- Up to 100GBASE-KR4 backplane support
- Two independent Layer 2/3+ Gigabit Ethernet switch matrices
- Designed to support two physically separated enclaves to facilitate security certification
- Speed, duplex, flow control, and power management on all ports
- Front panel 3x 1/10/40/100* Gbps Data Plane and 1/10 Gigabit Control Plane Ethernet ports access
- Future support for Time Sensitive Networking (TSN) via software update



Specification

General Characteristics
- Slot Profiles: SLT3-SWH-6F1U7U-14.4.14 and SLT3-SWH-5F4UG7U
- VLAN support for 802.1Q tagging, port mirroring, and MAC-based filtering and access control
- Ingress filtering
- Loop prevention and redundant path management via RSTP (IEEE 802.1w) and MSTP (IEEE 802.1s)
- Static IPv4/IPv6 routing available today, OSPFv2/v3and RIPv2/RIPng with remote routing table configuration on a roadmap
- QoS with 802.1p Priority Code Point and IP DSCP
- Access Control Lists (L2, L3, and L4)
- Multicast with IGMPv3 snooping (compatible with v1 and v2) and MLDv2 snooping (v1, v2)
- Authentication and access control supports local authentication, RADIUS/TACACS+ remote management authentication, and IEEE 802.1X Port-Based Network Access Control (PNAC)
- Link-layer security supports IEEE 802.1AR Device Identity (DevID) for secure device identification and encrypted Ethernet communications with 256-bitencryption and IEEE 802.1AE MACsec with 802.1XMACsec Key Agreement (MKA) in the near future
- Provision for future certificate-based authentication enhancements, e.g., EAP-TLS and X.509 credentials
- Console access via UART, SSH and Telnet over In-band and Out-of-band interfaces
- SNMP, Syslog, DHCP, DNS, and SNTP management services
- SNMPv1/v2c/v3 support with private MIB extensions
- Software configuration; GUI management and in-system upgrade
- System Management using Intelligent Platform Management Interface (IPMI)
- PBIT/BIST Status coming soon

Data Plane Characteristics
- 6x ports to the backplane: 4x supporting 1/10/40/100Gbps, and 2x 1/10/40 Gbps
- 3x ports to the front panel optical interfaces, two support 1/10/40/100 Gbps and one port supports1/10/40 Gbps
- Supports IEEE 802.3ap auto-negotiation, Reed-Solomon and Firecode forward error correction for10G/40G/100G port modes
- Ports configurable for lane speed, flow control, FEC, and breakout mode to affect power allocation
- Status telemetry includes CPU utilization, temperature, port Link/PHY/MAC state
- Statistics reporting for ports, VLANS RMON, ACL, and IEEE 802.1X authentication
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Control Plane Characteristics
- 7x 1/10 Gbps ports to the backplane
- 1x 1/10 Gbps port to front panel optical interface
- Supports IEEE 802.3ap auto-negotiation, Firecode forward error correction for 10G port mode
- Ports configurable for lane speed, flow control, and FEC to affect power allocation
- P2P/E2E transparent clock per IEEE 1588 PTPv2
- Hardware provision for Time Sensitive Networking (TSN) via software update

Black Enclave Data Plane Switch Matrix
- Marvell's Prestera® Aldrin2 device family

Red Enclave Control Plane Switch Matrix
- Marvell's Prestera® Ironman device family

Processing Unit
- Marvell's ARMADA® 385 high-performance dual core Cortex-A9 CPU System-on-Chip (SoC)

Network Management Software
- extensive CLI library for configuration, control, display, andtroubleshooting
- CLI consistent with IETF network protocols
- user searchable CLI commands

Other Backplane Interfaces
- 1x LVCMOS Maintenance Port

Power Specification
- requires 12 V and 3.3 V AUX from the backplane
- 100 W (max)

Environmental Specification
- conduction-cooled (VITA 48.2)
- operating shock:
  - VITA 47.1 Class OS2, 40g, 11ms half sine in all three axes
- operating vibration:
  - VITA 47.1 Class C3,
  - 40g, 5 Hz to 100 Hz PSD increasing at 3 dB/octave
  - 100 Hz to 1000 Hz power spectral density (PSD) = 0.1 g2/Hz
  - 1000 Hz to 2000 Hz PSD decreasing at 6 dB/octave
- operating temperature:
  - VITA 47.1 Class CC3, -40°C to +70°C (RCx-Series)
- Non-operating temperature:
  - VITA 47.1 Class NT2, -40°C to +85°C

Mechanical Specification
- ANSI/VITA 48.2-2010 compliant
- 3.94-inches x 7.18-inches (including clamshell and connectors)
- weight: 1 lb (including clamshell)

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